

Docket No.: TESSERA 3.3-018 CONT CONT II CONT II CON  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Khandros et al.

Application No.: 09/691,807

Group Art Unit: 2815

Filed: October 19, 2000

Examiner: S. Clark

For: STACKED CHIP ASSEMBLY

Commissioner for Patents  
Washington, DC 20231

# 7  
4-15-02  
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COMMUNICATION

Dear Madam:

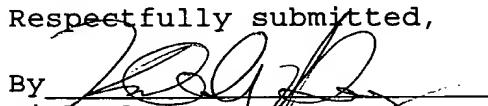
Per your request in the Official Action mailed December 19, 2001, Applicants enclose herewith copies of the references cited in the information disclosure statement filed with the U.S. Patent and Trademark Office on April 5, 2001. A response to the Official Action was filed March 19, 2002 under 37 C.F.R. § 1.8.

If there are any additional charges in connection with this requested communication, the Examiner is authorized to charge Deposit Account No. 12-1095 therefor.

If there are any questions, the Examiner can contact the undersigned.

Dated: March 20, 2002

Respectfully submitted,

By   
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